

## WEST Search History

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DATE: Tuesday, January 18, 2005

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	<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L44	l41 not L43	54
<input type="checkbox"/>	L43	(microinstruction\$1 or micro-instruction\$1 or microcode or microop\$1 or micro-op\$1) and l42	42
<input type="checkbox"/>	L42	l1 same (superscalar or vliw)	66
<input type="checkbox"/>	L41	l39 and (superscalar or vliw)	96
<input type="checkbox"/>	L40	l38 and L39	5
<input type="checkbox"/>	L39	(microinstruction\$1 or micro-instruction\$1 or microcode or microop\$1 or micro-op\$1) and l1	244
<input type="checkbox"/>	L38	l1 and L37	19
<input type="checkbox"/>	L37	712/23,24.ccls.	873
<input type="checkbox"/>	L36	superscalar and L35	9
<input type="checkbox"/>	L35	external adj (microinstruction\$1 or micro-instruction\$1 or microcode or microop\$1 or micro-op\$1)	59
<input type="checkbox"/>	L34	superscalar and L33	0
<input type="checkbox"/>	L33	L32 not L31	139
<input type="checkbox"/>	L32	eeeprom same (microinstruction\$1 or micro-instruction\$1 or microcode or microop\$1 or micro-op\$1)	228
<input type="checkbox"/>	L31	eeeprom with (microinstruction\$1 or micro-instruction\$1 or microcode or microop\$1 or micro-op\$1)	89
<input type="checkbox"/>	L30	L25 and integer unit	1
<input type="checkbox"/>	L29	L25 and floating point unit	1
<input type="checkbox"/>	L28	L25 and functional units	18
<input type="checkbox"/>	L27	L25 with floating point unit	0
<input type="checkbox"/>	L26	L25 with functional units	0
<input type="checkbox"/>	L25	flash memory with L8	1294
<input type="checkbox"/>	L24	L23 not L22	2
<input type="checkbox"/>	L23	(eeeprom with L8) and integer unit	2
<input type="checkbox"/>	L22	(eeeprom with L8) and floating point unit	2
<input type="checkbox"/>	L21	L20 not L11	0
<input type="checkbox"/>	L20	(eeeprom with L8) and functional units	21
<input type="checkbox"/>	L19	L18 not L17	6
<input type="checkbox"/>	L18	L8 with eprom with external	15
<input type="checkbox"/>	L17	L8 with external eprom	9

<input type="checkbox"/>	L16	L8 with eprom	916
<input type="checkbox"/>	L15	external microinstruction and functional units	5
<input type="checkbox"/>	L14	external microinstructions and functional units	0
<input type="checkbox"/>	L13	external microcode and functional units	10
<input type="checkbox"/>	L12	external eeprom and functional units	4
<input type="checkbox"/>	L11	L9 and functional units	21
<input type="checkbox"/>	L10	external with L9	30
<input type="checkbox"/>	L9	eeprom with L8	1125
<i>DB=USPT,PGPB; PLUR=NO; OP=ADJ</i>			
<input type="checkbox"/>	L8	firmware or nanoinstruction\$1 or nano-instruction\$1 or nanocode or nano-code or nanoprogram or nano-program or L7	54582
<input type="checkbox"/>	L7	microinstruction\$1 or micro-instruction\$1 or microcode or micro-code or micro-operation\$1 or microoperation\$1 or micro-op\$1 or microop\$1 or micro-program\$1 or microprogram\$1	14542
<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>			
<input type="checkbox"/>	L6	eeprom and L2	34
<input type="checkbox"/>	L5	functional unit and L2	1
<input type="checkbox"/>	L4	functional units and L2	0
<input type="checkbox"/>	L3	functional units with L2	0
<input type="checkbox"/>	L2	5097445.uref.	55
<input type="checkbox"/>	L1	embedded processor\$1	2913

END OF SEARCH HISTORY